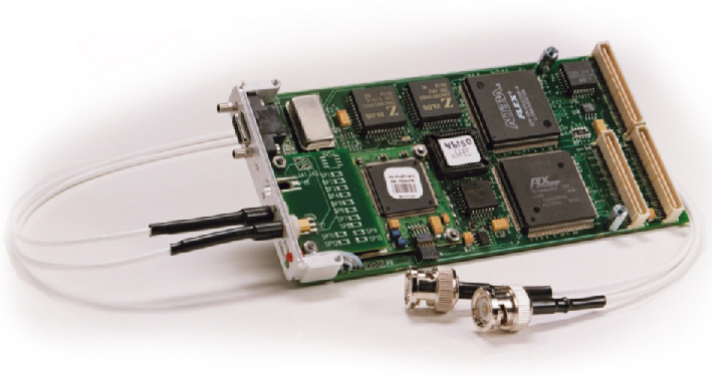
Time & Frequency Solutions

PMC-SyncClock32

Bus-Level Timing Board with On-board GPS

The PMC-SyncClock32 with On-board GPS is an advanced Mezzanine Card (PMC) module featuring an on-board GPS Receiver.



Features

- GPS Synchronisation standard
- Propagation delay compensation
- Zero latency time reads
- Match Time output
- IRIG B time code output
- External Event time tags
- Three user programmable rates

Key Benefits

Precision time is provided to the host computer with zero latency. The on-board micro-processor automatically synchronizes the clock to the GPS input signal. The PMC will also synchronize to an external 1PPS reference signal. Alternatively, the clock in the PMC can be set using commands from host computer and free run using its on-board oscillator as the time base.

When synchronizing to GPS or the 1 PPS input the micro-processor constantly measures the time error between the on-board clock and the GPS reference input and adjusts the error measurement for propagation delay. When the disciplined TCXO option is selected the residual error is used in an adaptive gain loop to adjust the frequency of the 10 MHz oscillator for minimum error. Before being used as the time reference, the GPS receiver must be locked to the GPS input. If the incoming GPS fails or is corrupted by noise the on-board clock is updated by the 10 MHz oscillator. When the GPS input signal is again useable the correction loop is smoothly closed.

58 bits of BCD time are available to the host computer using two zero latency time reads. The time message contains units of microseconds through units of years. A status word is available using an additional read.

The time-of-occurrence of external events may be captured (time-tagged) by using the Event Time input. When the event input is sensed the current time is saved in a buffer for later interrogation by the host. The resolution of the time tag is 100 nanoseconds.

The Match Time feature may be used to automatically initiate or terminate an external process. The resolution of the Match Time comparison is one microsecond. The Match Time output is asserted when the time of the internal clock matches that of the user input start time. The Match Time output may be terminated by a user command or when the previously set stop time is encountered.

Three user programmable pulse rates are provided. Two pulse rates, Clock Low and Clock High, are available on the multi-pin connector. The third pulse rate provides heartbeat timing to the host computer and is also available on the multi-pin connector. The divider for each of the three pulse rate generators is programmable by the host computer over the range 2–65,535. The inputs to the rate generators are 3 MHz or 100 Hz for the heartbeat, 100 PPS for Clock Low and 3 MHz for Clock High.

The GPS synchronization feature offers worldwide time transfer capability to the PMC-SyncClock32. Very precise synchronization, automatic leap year and leap second correction, plus accurate position information are additional benefits provided by the GPS option.

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PMC-SyncClock32 GPS Specifications

General Input Specifications

GPS Synchronisation	C/A code
Sync Accuracy	100 nanoseconds
Position Accuracy	25 metres SEP
Tracking	12 Parallel channels
Antenna*	L1, 25' lead-in cable
Fixed or Dynamic	Operating mode selected at time of order
1PPS Sync Input	RS422 or TTL, positive edge
1PPS Sync Accuracy	300 nanoseconds
External Event	
Resolution	100 nanoseconds–units year
Min. event spacing	None

General Output Specifications

IRIG B DC Shift	TTL
Match Pulse	TTL level at Start-Stop time
Resolution	Microseconds-eight millisecind
Clock Low	TTL, negative going
Clock Divisor	2–65,535
Clock Input	100 PPS
Default output	1 PPS
Clock High	TTL, negative going
Clock Divisor	2–65,535
Clock Input	3 MPPS
Default output	76.923k PPS
Heartbeat Rate	Interrupt, flag, TTL, negative going
Clock Divisor	2–65,535
Clock Input	100 PPS or 3 MPPS
Default output	1k PPS
BCD Time	Microseconds–unit year on demand, zero latency 58 bits in two 32 bit words
Status word	8 bits
Status LED	Flashes coded patterns
Interrupts	External Event, RAM FIFO, Heartbeat, Match Time
Flags	Dual Port RAM data ready, FIFO data ready, In sync, Heartbeat, Match Time, External Event
Connectors	BNC, high density DB-26

MTBF

141,000 Hours per Mil-217-F, Notice 2, 25°C, ground benign

Mechanical & Environmental

Size	74mm X 149mm single CMC
Type	Single-slot 32 bit 5V PCI
Power	
+5Vdc	±5%, 150 mA maximum
+12 Vdc	±5%, 60 mA maximum
-12 Vdc	±5%, 25 mA maximum
Operating Temperature	0°C to +70°C
Storage Temperature	-40°C to +85°C
Humidity	To 95% without condensation

Options

Antenna Options*	
Mast mount antenna	includes 100 feet co-ax cable
Low loss cable & amplifiers	
Fiber Optics	Fiber Tx/Rx pair for long runs or secure environments
FAA vertified antennas	For aircraft
Differential GPS Inputs	Per RTCM 104
IRIG-B Modulated Output	2.5 Vpp into 60 Ohms
Input Code Isolation	Transformer coupling
Input Codes	IRIG G, XR3, 2137, IRIG E, 109-60
Output codes	IRIG A, NASA 36,
Eight External Event Inputs	TTL positive or negative edge
FIFO	For external events
Have Quick Output	Per ICD–GPS–060
Binary Time Words	Replaces BCD
Oscillator Upgrade	Disciplined TCXO, 1 PPM

*consult factory for cable length choice

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